

Notice of References Cited	Application/Control No. 09/834,342	Applicant(s)/Patent Under Reexamination LAHIRI ET AL.	
	Examiner Fred Ferris	Art Unit 2128	Page 1 of 1

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	B	US-5,940,618 A	08-1999	Blandy et al.	717/128
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NON-PATENT DOCUMENTS

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	V	"Automatic and Efficient Evaluation of Memory Hierarchies for Embedded Systems", S. Abraham et al, Hewlett-Packard # HPL 1999-132, October 1999
	W	"Active Memory: A New Abstraction for Memory system Simulation", A.R. Lebeck et al, ACM Transactions on Modeling and Computer Simulation, Vol. 7, No. 1, January 1997
	X	"Retargetable Cache Simulation Using High Level Processor Models", R. Ravindran et al, IEEE 0-7695-0954-1/01, IEEE January 2001

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.